

CERN ALICE DAQ

Detector Data Link (DDL) Interface Cards

v. 2.1

Schematic Diagrams

MEDIA INTERFACE
media_interface.sch



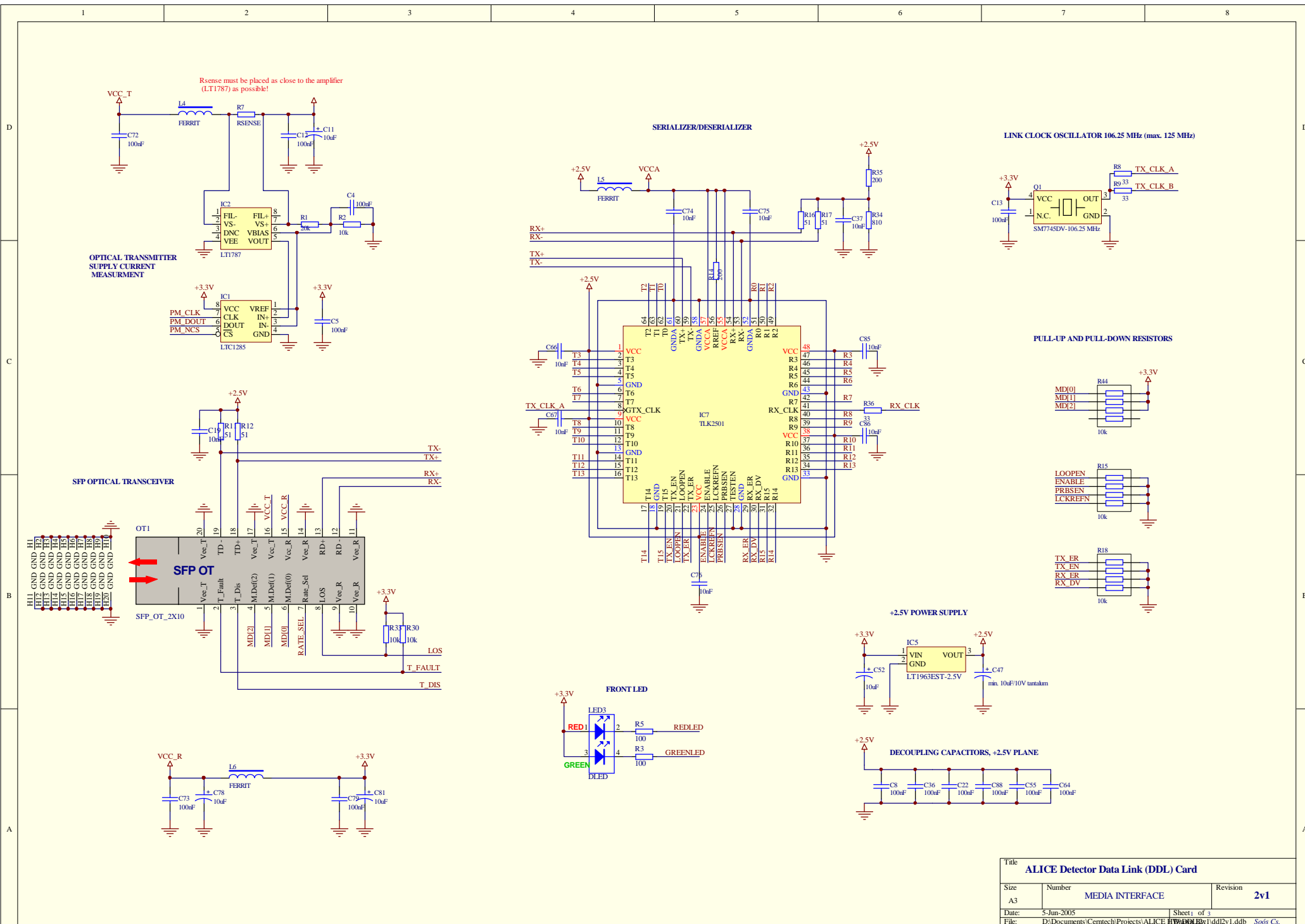
ALTERA FPGA
altera.sch



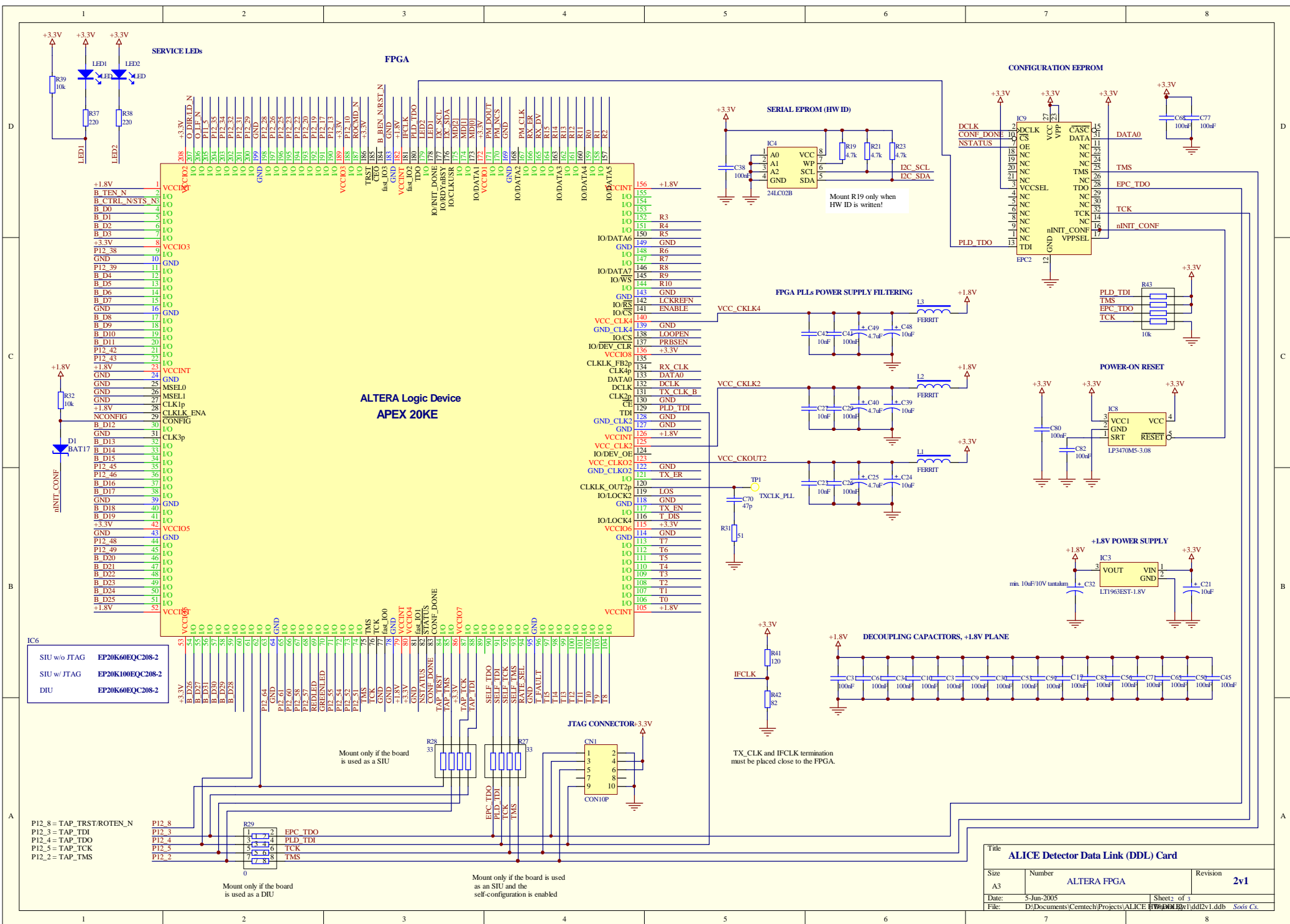
DDL INTERFACE
ddl_interface.sch



Title		
Size A4	Number	Revision
Date: 5-Jun-2005	Sheet of	
File: D:\Documents\Cemtech\Projects\ALICE HW\DDL\ddl2v1.ddb	1 of 1	

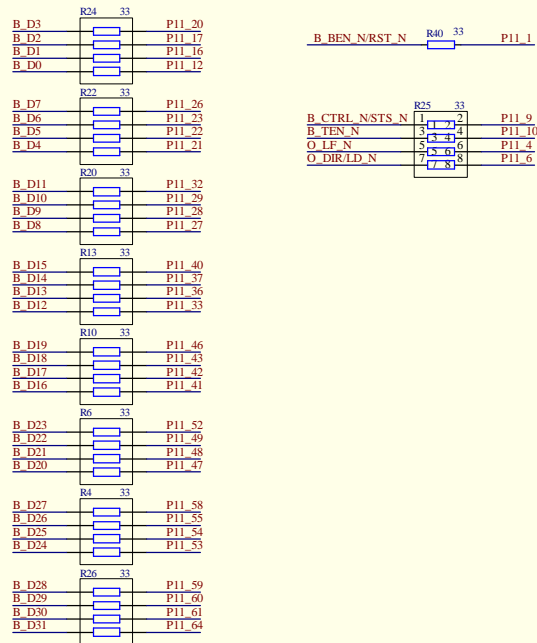


Title			
ALICE Detector Data Link (DDL) Card			
Size	Number	Revision	2v1
A3	MEDIA INTERFACE		
Date:	5-Jun-2005	Sheet 1 of 3	
File:	D:\Documents\Cemtech\Projects\ALICE HW\HW1\ddl2v1.ddb Soós Cs.		

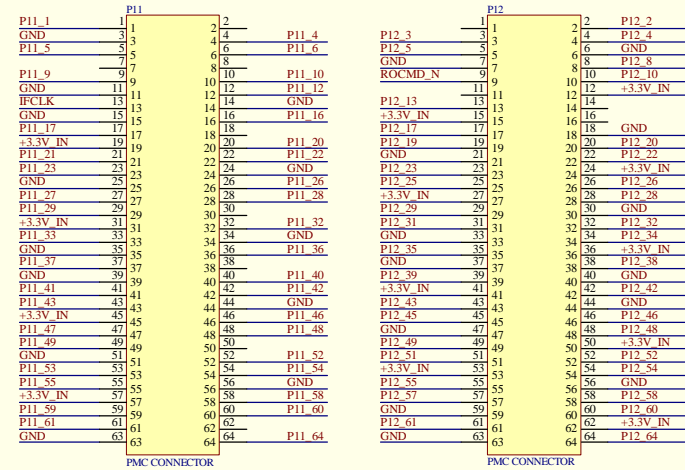


Title			
ALICE Detector Data Link (DDL) Card			
Size	Number	Revision	2v1
A3	ALTEA FPGA		
Date:	5-Jun-2005	Sheet 2 of 3	
File:	D:\Documents\Cemtech\Projects\ALICE HW\HW1\Bv1\ddl2v1.ddb Sois Cx		

SERIAL RESISTORS ON INTERFACE SIGNALS

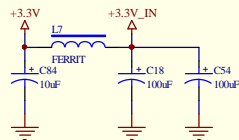


DDL INTERFACE CONNECTORS

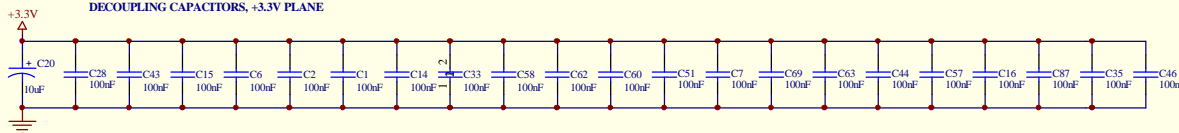


DIU	SIU	DIU	SIU
P11_1	RORST_N	FIBEN_N	FIBEN_N
P11_4	RILF_N	FILF_N	FILF_N
P11_5	ROBSY_N	FOBSY_N	FOBSY_N
P11_6	RILD_N	FIDIR	FIDIR
P11_9	RISTS_N	FBCTRL_N	FBCTRL_N
P11_10	RITEN_N	FBTEN_N	FBTEN_N
P11_12	RID0	FBD0	FBD0
P11_16	RID1	FBD1	FBD1
P11_17	RID2	FBD2	FBD2
P11_20	RID3	FBD3	FBD3
P11_21	RID4	FBD4	FBD4
P11_22	RID5	FBD5	FBD5
P11_23	RID6	FBD6	FBD6
P11_26	RID7	FBD7	FBD7
P11_27	RID8	FBD8	FBD8
P11_28	RID9	FBD9	FBD9
P11_29	RID10	FBD10	FBD10
P11_32	RID11	FBD11	FBD11
P11_36	RID12	FBD12	FBD12
P11_37	RID13	FBD13	FBD13
P11_40	RID14	FBD14	FBD14
P11_41	RID16	FBD16	FBD16
P11_42	RID17	FBD17	FBD17
P11_43	RID18	FBD18	FBD18
P11_46	RID19	FBD19	FBD19
P11_47	RID20	FBD20	FBD20
P11_48	RID21	FBD21	FBD21
P11_49	RID22	FBD22	FBD22
P11_52	RID23	FBD23	FBD23
P11_53	RID24	FBD24	FBD24
P11_54	RID25	FBD25	FBD25
P11_55	RID26	FBD26	FBD26
P11_58	RID27	FBD27	FBD27
P11_59	RID28	FBD28	FBD28
P11_60	RID29	FBD29	FBD29
P11_61	RID30	FBD30	FBD30
P11_64	RID31	FBD31	FBD31

INPUT POWER FILTERING



DECOUPLING CAPACITORS, +3.3V PLANE



Title ALICE Detector Data Link (DDL) Card			
Size A3	Number DDL INTERFACE	Revision 2v1	
Date: 5-Jun-2005	Sheet 3 of 3		
File: D:\Documents\Cemtech\Projects\ALICE HW\HW11B\11ddl2v1.ddb SoCs Cx			